

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	65	((write near2 command) or (write near2 request)) near6 address near6 (compar\$3 or check\$3) near4 (buffer or cache or queue))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:36
L3	103	(requested near2 address) near6 (compar\$3 or check\$3) near6 (address near3 (buffer or cache or queue))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:37
L4	89	(requested near2 address) near6 (compar\$3 or check\$3) near3 (address near3 (buffer or cache or queue))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:37
L5	33	(requested adj address) near6 (compar\$3 or check\$3) near3 (address near3 (buffer or cache or queue))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:10
L6	1	write with ((requested adj address) near6 (compar\$3 or check\$3) near3 (address near3 (buffer or cache or queue)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:11
S1	1	"20040123021"	US-PGPUB	ADJ	ON	2005/09/20 10:04
S6	534	(controller or processor or cpu or master) near16 (select\$2 or set\$3) near16 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:13
S7	343	((controller or processor or cpu or master) near6 (select\$2 or set\$3)) near16 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:11
S11	109	S7 and 711/137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:10

S12	255	((controller or processor or cpu or master) near6 (select\$2 or set\$3)) near6 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:11
S13	94	S12 and 711/137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:11
S14	287	(plurality or multiple or several) near5 (controller or processor or cpu or master) near16 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:14
S15	20	((plurality or multiple or several) near5 (controller or processor or cpu or master) near16 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3)) with select\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:18
S16	343	((plurality or multiple or several or multi-) near5 (controller or processor or cpu or master) near16 (prefetch\$3 or pre-fetch\$3 or prered\$3 or pre-read\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:19
S17	91	S16 and 711/137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 10:19
S18	12	((select\$3 or choos\$3 or setting or designat\$2) near4 (controller or processor or cpu or master)) near8 (stor\$3 or load\$3 or saving) near6 (buffer or cache or store) near16 (pre-fetch\$3 or prefetch\$3 or pre-read\$3 or prered\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:03
S20	31	((select\$3 or choos\$3 or setting or designat\$2) near8 (controller or processor or cpu or master)) with (stor\$3 or load\$3 or saving) near10 (buffer or cache or store) with (pre-fetch\$3 or prefetch\$3 or pre-read\$3 or prered\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 11:35

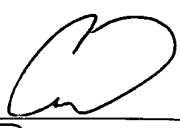
S21	19	S20 not S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 11:36
S22	2634	(arbitrar\$3 or random\$2) near2 (choos\$3 or select\$3 or pick\$3 or setting or designat\$3) near5 (cpu or master or processor or controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 12:42
S23	24	(arbitrar\$3 or random\$2) near2 (choos\$3 or select\$3 or pick\$3 or setting or designat\$3) near5 (cpu or master or processor or controller) and (pre-fetch\$3 or prefetch\$3 or preread\$3 or pre-read\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 12:40
S24	1647	(arbitrar\$3 or random\$2) near2 (select\$3) near3 (cpu or master or processor or controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 12:44
S25	1	711/137 and S24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 12:43
S26	140	(arbitrar\$3 or random\$2) near2 (select\$3) near3 (plurality) near3 (cpu or master or processor or controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 13:30
S27	0	(arbitrar\$3 or random\$2) near2 (select\$3) near3 (plurality) near3 (cpu or master or processor or controller) and cahce	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 13:30
S28	15	(arbitrar\$3 or random\$2) near2 (select\$3) near3 (plurality) near3 (cpu or master or processor or controller) and cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 13:30

S29	1513	(prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3) and (flag or indicator) and (address) near6 (compar\$3) and (store or cache or buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 14:58
S30	1099	(prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3) and (flag or indicator) and (store or cache or buffer) and ((address) near2 (compar\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 14:58
S31	1248	711/137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 14:58
S32	78	S30 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/20 14:58
S33	31678	(determin\$5 or predetermin\$5 or pre-determin\$5) near8 (controller or processor or cpu or master) near8 (select\$3 or choos\$3 or setting or designat\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:07
S34	1173	(determin\$5 or predetermin\$5 or pre-determin\$5) near6 (controller or processor or cpu or master) near6 (select\$3 or choos\$3 or setting or designat\$2) near6 (command or request)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:14
S35	4561	(711/150 or 711/151 or 711/152 or 711/153 or 711/163)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:30
S36	29	S34 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:08

S37	16	(determin\$5 or predetermin\$5 or pre-determin\$5) near6 (controller or processor or cpu or master) near6 (select\$3 or choos\$3 or setting or designat\$2) near6 (command or request) with ((stor\$3 or load\$3 or saving) near6 (buffer or cache or store))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:19
S38	227	(determin\$5 or predetermin\$5 or pre-determin\$5) near6 (controller or processor or cpu or master) near6 (select\$3 or choos\$3 or setting or designat\$2) with ((stor\$3 or load\$3 or saving) near6 (buffer or cache or store))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:24
S39	212608	(controller or processor or cpu or master) near8 (command or request)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:27
S40	197595	((select\$3 or choos\$3 or setting or designat\$2) near4 (controller or processor or cpu or master))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:32
S41	31678	(determin\$5 or predetermin\$5 or pre-determin\$5) near8 (controller or processor or cpu or master) near8 (select\$3 or choos\$3 or setting or designat\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:28
S42	515155	(stor\$3 or load\$3 or saving) near8 (buffer or cache or store)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:30
S43	4619	S39 and S40 and S41 and S42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:30
S44	5648	(711/150 or 711/151 or 711/152 or 711/153 or 711/163 or 711/137)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:30

S45	124	S43 and S44	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:30
S46	5	((determin\$5 or predetermin\$5 or pre-determin\$5) near6 ((select\$3 or choos\$3 or setting or designat\$2) near4 (controller or processor or cpu or master)) near6 (command or request)) with ((stor\$3 or load\$3 or saving) near6 (buffer or cache))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:36
S47	27	((determin\$5 or predetermin\$5 or pre-determin\$5) near10 ((select\$3 or choos\$3 or setting or designat\$2) near8 (controller or processor or cpu or master)) near10 (command or request)) with ((stor\$3 or load\$3 or saving) near8 (buffer or cache))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 10:36
S48	22	S47 not S46	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 11:08
S49	4	(select\$3 or choos\$3) near4 (controller or processor or cpu or master or host or client) near6 (stor\$3 or load\$3) near6 (prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3) near4 data near4 (buffer or queue or cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 11:11
S50	39	(select\$3 or choos\$3) with (controller or processor or cpu or master or host or client) with (stor\$3 or load\$3) with (prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3) with data with (buffer or queue or cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 11:14
S51	35	S50 not S49	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 11:11

S52	6	((select\$3 or choos\$3) near2 (controller or processor or cpu or master or host or client)) with ((stor\$3 or load\$3) near6 (prefetch\$3 or pre-fetch\$3 or preread\$3 or pre-read\$3) near3 data) near6 (buffer or queue or cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 11:15
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